



Evaluator Group

Architectural Considerations to Reduce AI Training Time and Accelerate Intelligence

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August 14, 2018

An award-winning effort at CERN has demonstrated potential to significantly change how the physics based modeling and simulation communities view machine learning. The CERN team demonstrated that AI-based models have the potential to act as orders-of-magnitude-faster replacements for computationally expensive tasks in simulation, while maintaining a remarkable level of accuracy.

Dr. Federico Carminati (Project Coordinator, CERN) points out, "This work demonstrates the potential of 'black box' machine-learning models in physics-based simulations."

A poster describing this work was awarded the prize for best poster in the category 'programming models and systems software' at ISC'18. This recognizes the importance of the work, which was carried out by Dr. Federico Carminati, Gul Rukh Khattak, and Dr. Sofia Vallecorsa at CERN, as well as Jean-Roch Vlimant at Caltech. The work is part of a CERN openlab project in collaboration with Intel Corporation, who partially funded the endeavor through the Intel Parallel Computing Center (IPCC) program.

Widespread potential impact for simulation

The world-wide impact for High-Energy Physics (HEP) scientists could be substantial, as outlined by the CERN poster, which points out that "Currently, most of the LHC's worldwide distributed CPU budget — in the range of half a million CPU-years equivalent — is dedicated to simulation." Speeding up the most time-consuming simulation tasks (e.g., high-granularity calorimeters, which are components in a detector that measure the energy of particles[i]) will help scientists better utilize these allocations. The following are comparative results obtained by the CERN team in the time to create an electron shower, once the AI model has been fully trained:

Dr. Sofia Vallecorsa points out that the CPU based runtime is important as nearly all of the Geant user base runs on CPUs. Vallecorsa is a CERN physicist who was also highlighted in the CERN article *Coding has no gender* (<https://home.cern/about/updates/2018/02/coding-has-no-gender>).

As scientists consider future CERN experiments, Vallecorsa observes, "Given future plans to upgrade CERN's Large Hadron Collider, dramatically increasing particle collision rates, frameworks like this have the potential to play an important role in ensuring data rates remain manageable."

This kind of approach could help to realize similar orders-of-magnitude-faster speedups for computationally expensive simulation tasks used in a range of fields.

Vallecorsa explains that the data distributions coming from the trained machine-learning model are remarkably close to the real and simulated data.

A big change in thinking

The team demonstrated that "energy showers" detected by calorimeters can be interpreted as a 3D image[ii]. The process is illustrated in the following figure. The team adopted this approach from the machine-learning community as deep-learning convolutional neural networks are heavily utilized when working with images.

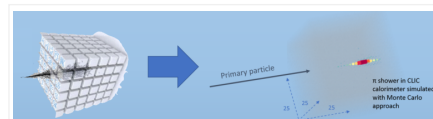
Use of GANS

The CERN team decided to train Generative Adversarial Networks (GANs) on the calorimeter images. GANs are particularly suited to act as a replacement for the expensive Monte Carlo methods used in HEP simulations as they generate realistic samples for complicated probability distributions, allow multi-modal output, can do interpolation, and are robust against missing data.

The basic idea is easy to understand: train a Generator (G) to create the calorimeter image with sufficient accuracy to trick a discriminator (D) which tries to identify artificial samples from the generator compared to real samples from the Monte Carlo simulation. G reproduces the data distribution starting from random noise. D estimates the probability that a sample came from the training data rather than G. The training procedure for G is to maximize the probability of D making a mistake. A high-level illustration of the GAN is provided below.

Time to create an electron shower		
Method	Machine	Time/Shower (msec)
Full Simulation (geant4)	Intel Xeon Platinum 8180	17000
3d GAN (batch size 128)	Intel Xeon Platinum 8180	7

(https://6lli539m39y3hpkelqsm3c2fg-wpengine.netdna-ssl.com/wp-content/uploads/2018/08/hpc_creating_electro)
 Figure 1: Comparative runtime to create an electron shower of the machine-learning method (e.g. 3d GAN) vs. the full Monte-Carlo simulation (Image courtesy CERN)



(https://6lli539m39y3hpkelqsm3c2fg-wpengine.netdna-ssl.com/wp-content/uploads/2018/08/hpc_single_particle_elec)
 Figure 2: Schematic from the poster showing how a single particle creates an electron shower that can be viewed as an image (Courtesy CERN)

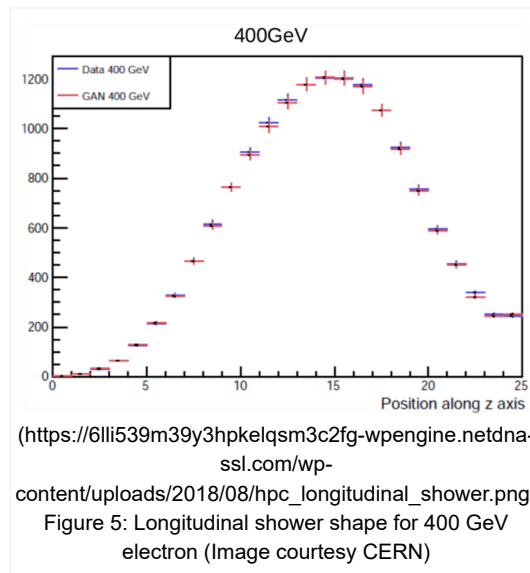
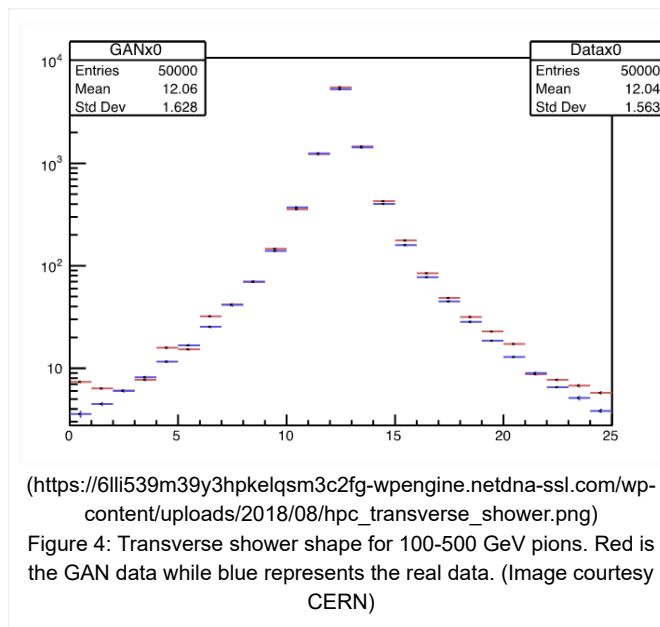
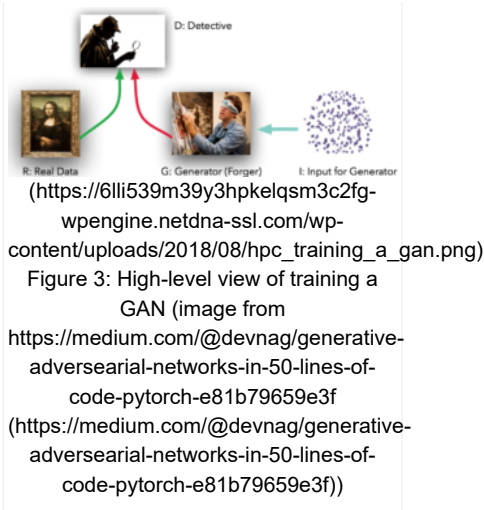
Even though the description is simple, 3D GANs are unfortunately not “out-of-the-box” networks, which meant the training of the model was non-trivial.

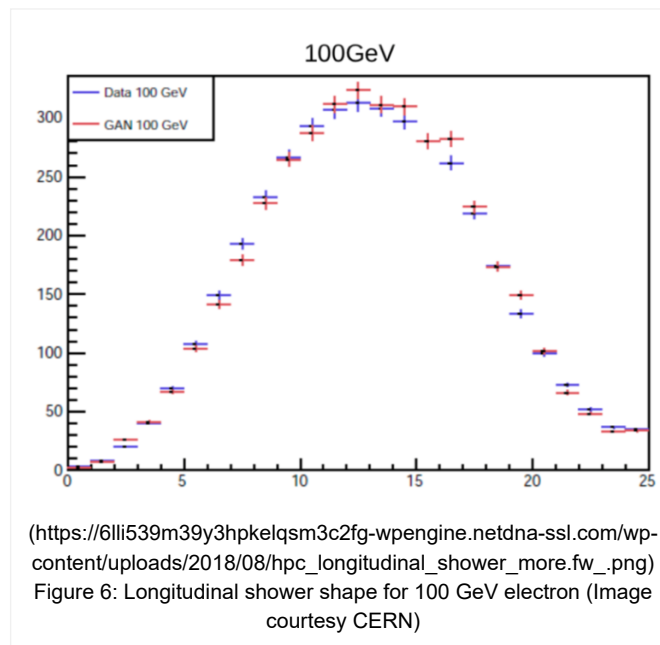
Results

After detailed validation of the trained GAN, there was “remarkable” agreement between the images from the generator and the Monte-Carlo images. This type of approach could potentially be beneficial in other fields where Monte Carlo simulation is used.

More specifically, the CERN team compared high level quantities (e.g., energy shower shapes) and detailed calorimeter response (e.g., single cell response) between the trained generator and the standard Monte Carlo. The CERN team describes the agreement, which is within a few percent, as “remarkable” in their poster.

Visually this agreement can be seen by how closely the blue (real data) and red lines (GAN generated data) overlap in the following results reported in the poster.





Vallecorsa summarizes these results by stating, “The agreement between the images generated by our model and the Monte Carlo images has been beyond our expectations. This demonstrates that this is a promising avenue for further investigation.”

CERN openlab

The CERN team plans to test performance using FPGAs and other integrated accelerator technologies. FPGAs are known to deliver lower latency and higher inferencing performance than both CPUs and GPUs[[ii](#)]. The CERN group also intends to test several deep learning techniques in the hope of achieving a yet greater speedup with respect to Monte Carlo techniques, and ensuring this approach covers a range of detector types, which CERN believes is key to future projects.

This research is being carried out through a [CERN openlab](https://openlab.cern/) (<https://openlab.cern/>) project. CERN openlab is a public-private partnership through which CERN collaborates with leading ICT companies to drive innovation in cutting-edge ICT solutions for its research community. Intel has been a partner in CERN openlab since it was first established in 2001. Dr. Alberto Di Meglio (Head of CERN openlab) observes, “At CERN, we’re always interested in exploring upcoming technologies that can help researchers to make new ground-breaking discoveries about our universe. We support this through joint R&D projects with our collaborators from industry, and by making cutting-edge technologies available for evaluation by researchers at CERN.”

Summary

The HPC modeling and simulation community now has a promising path forward to exploit the benefits of machine learning. The key, as demonstrated by CERN, is that the machine-learning-generated distribution needs to be indistinguishable from other high-fidelity methods in physics-based simulations.

The motivation is straightforward: (1) orders of magnitude faster performance, (2) efficient CPU implementations, and (3) this approach could enable the use of other new technologies such as FPGAs that may significantly improve performance.

Additional References

- The award-winning CERN openlab poster (https://openlab.cern/sites/openlab.web.cern.ch/files/2018-06/Vallecorsa_poster.pdf).
- Goodfellow et al. 2014 (<https://arxiv.org/abs/1406.2661>)
- Conditional GAN, arXiv: 1411.1784 (<https://arxiv.org/abs/1411.1784>)
- Auxiliary Classifier GAN, arXiv:1610.0958 (<https://arxiv.org/abs/1610.09585>)
- The CERN team noted that all tests were run with Intel optimised Tensorflow (<https://software.intel.com/en-us/articles/intel-optimization-for-tensorflow-installation-guide>)4.1. + keras (<https://keras.io/>) 2.1.2

Rob Farber is a global technology consultant and author with an extensive background in HPC and in machine learning technology that he applies at national labs and commercial organizations on a variety of problems including challenges in high energy physics. Rob can be reached at info@techenablement.com (<mailto:info@techenablement.com>).

[i] <http://cds.cern.ch/record/2254048#> (<http://cds.cern.ch/record/2254048>)

[ii] *ibid*

[iii] <https://medium.com/syncedreview/deep-learning-in-real-time-inference-acceleration-and-continuous-training-17dac9438b0b> (<https://medium.com/syncedreview/deep-learning-in-real-time-inference-acceleration-and-continuous-training-17dac9438b0b>).

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Off The Wire

Industry Headlines



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- 2 Supercomputer Noctua Inaugurated at Paderborn University (https://www.hpcwire.com/off-the-wire/supercomputer-noctua-inaugurated-at-paderborn-university/)
- 3 Institute for Basic Science in South Korea Awards Cray Contract to Help Advance Research (https://www.hpcwire.com/off-the-wire/institute-for-basic-science-in-south-korea-awards-cray-contract-to-help-advance-research/)
- 4 Julia Computing's Jane Herriman Joins NumFOCUS Board of Directors (https://www.hpcwire.com/off-the-wire/julia-computings-jane-herriman-joins-numfocus-board-of-directors/)
- 5 ACM Announces New Charles P. "Chuck" Thacker Breakthrough in Computing Award (https://www.hpcwire.com/off-the-wire/acm-announces-new-charles-p-chuck-thacker-breakthrough-in-computing-award/)
- 6 Intel Adds to Portfolio of FPGA Programmable Acceleration Cards to Speed Up Data Center Computing (https://www.hpcwire.com/off-the-wire/intel-adds-to-portfolio-of-fpga-programmable-acceleration-cards-to-speed-up-data-center-computing/)
- 7 Colibra Partners with San Diego Supercomputer Center and Others to Launch New BlockLAB Research Laboratory (https://www.hpcwire.com/off-the-wire/colibra-partners-with-san-diego-supercomputer-center-and-others-to-launch-new-blocklab-research-laboratory/)
- 8 Denmark Commits to Joining EuroHPC Joint Undertaking (https://www.hpcwire.com/off-the-wire/denmark-commits-to-joining-eurohpc-joint-undertaking/)
- 9 University of Vermont Awarded Grant for Supercomputer (https://www.hpcwire.com/off-the-wire/university-of-vermont-awarded-grant-for-supercomputer/)
- 10 Intel Participates in White House Summit on Advancing US Leadership in Quantum Computing (https://www.hpcwire.com/off-the-wire/intel-participates-in-white-house-summit-on-advancing-us-leadership-in-quantum-computing/)

September 24, 2018

- 1 Azure HDInsight Brings Next Generation Hadoop 3.0 and Enterprise Security to the Cloud (https://www.hpcwire.com/off-the-wire/azure-hdinsight-brings-next-generation-hadoop-3-0-and-enterprise-security-to-the-cloud/)
- 2 Ethernet Forges Ahead with Successful Ethernet Alliance Plugfest (https://www.hpcwire.com/off-the-wire/ethernet-forges-ahead-with-successful-ethernet-alliance-plugfest/)

- NSF Office of Advanced Cyberinfrastructure Announces Search to Fill Several Program Director Positions (<https://www.hpcwire.com/off-the-wire/nsf-office-of-advanced-cyberinfrastructure-announces-search-to-fill-several-program-director-positions/>)
- Cornell Named Training Partner on \$60 Million Frontera Supercomputer Project (<https://www.hpcwire.com/off-the-wire/cornell-named-training-partner-on-60-million-frontera->

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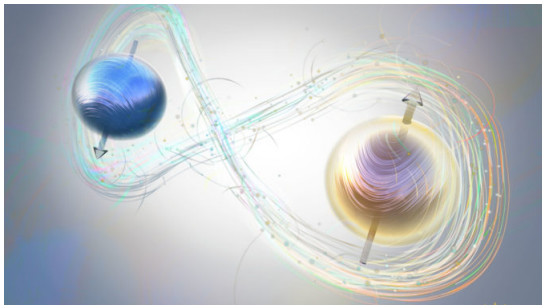


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Quantum Rolls – DOE Dishes \$218M; NSF Awards \$31M; US Releases ‘Strategic Overview’
 (<https://www.hpcwire.com/2018/09/24/quantum-rolls-doe-dishes-218m-nsf-awards-31m-u-s-releases-strategic-overview/>)

It was quite a day for U.S. quantum computing. In conjunction with the White House Summit on Advancing American Leadership in Quantum Information Science (QIS) held to [31m-u-s-releases-strategic-overview/](https://www.hpcwire.com/2018/09/24/quantum-rolls-doe-dishes-218m-nsf-awards-31m-u-s-releases-strategic-overview/)

By John Russell

[http://twitter.com/intent/tweet?](http://twitter.com/intent/tweet?status=Quantum%20Rolls%20%E2%80%93%20DOE%20Dishes%20%24218M%3B%20NSF%20Awards%20%2431M%3B%20US%20Releases%20%24218m-nsf-awards-31m-u-s-releases-strategic-overview%2F)

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Russian and American Scientists Achieve 50% Increase in Data Transmission Speed

(<https://www.hpcwire.com/2018/09/20/russian-and-american-scientists-achieve-50-increase-in-data-transmission-speed/>)

As high-performance computing becomes increasingly data-intensive and the demand for shorter turnaround times grows, data transfer speed becomes an ever more important factor. ([increase-in-data-transmission-speed/](https://www.hpcwire.com/2018/09/20/russian-and-american-scientists-achieve-50-increase-in-data-transmission-speed/))

By Oliver Peckham

<http://twitter.com/intent/tweet?status=Russian%20and%20American%20Scientists%20Achieve%2050%25%20Increase%20in%20Data%20Transmission%20Speed%2F>

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IBM to Brand Rescale's HPC-in-Cloud Platform

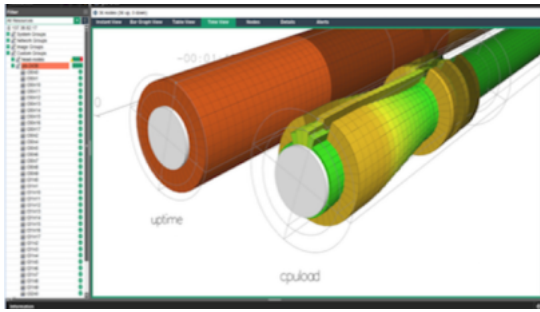
(<https://www.hpcwire.com/2018/09/20/ibm-to-brand-rescales-hpc-in-cloud-platform/>)

HPC (or big compute)-in-the-cloud platform provider Rescale has formalized the work it's been doing in partnership with public cloud vendors by announcing its Powered by Rescale HPC-in-Cloud Platform. ([By Doug Black](https://www.hpcwire.com/2018/09/20/ibm-to-brand-rescales-hpc-in-cloud-platform/))

<http://twitter.com/intent/tweet?status=IBM%20to%20Brand%20Rescale%20%E2%80%93%20HPC-in-Cloud%20Platform+https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F20%2Fibm-to-brand-rescales-hpc-in-cloud-platform%2F&title=IBM%20to%20Brand%20Rescale%20%E2%80%93%20HPC-in-Cloud%20Platform>

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IBM Accelerated Insights



Clouds Over the Ocean – a Healthcare Perspective (https://www.hpcwire.com/solution_content/ibm/life-sciences/c)

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Democratization of HPC Part 1: Simulation Sheds Light on Building Dispute

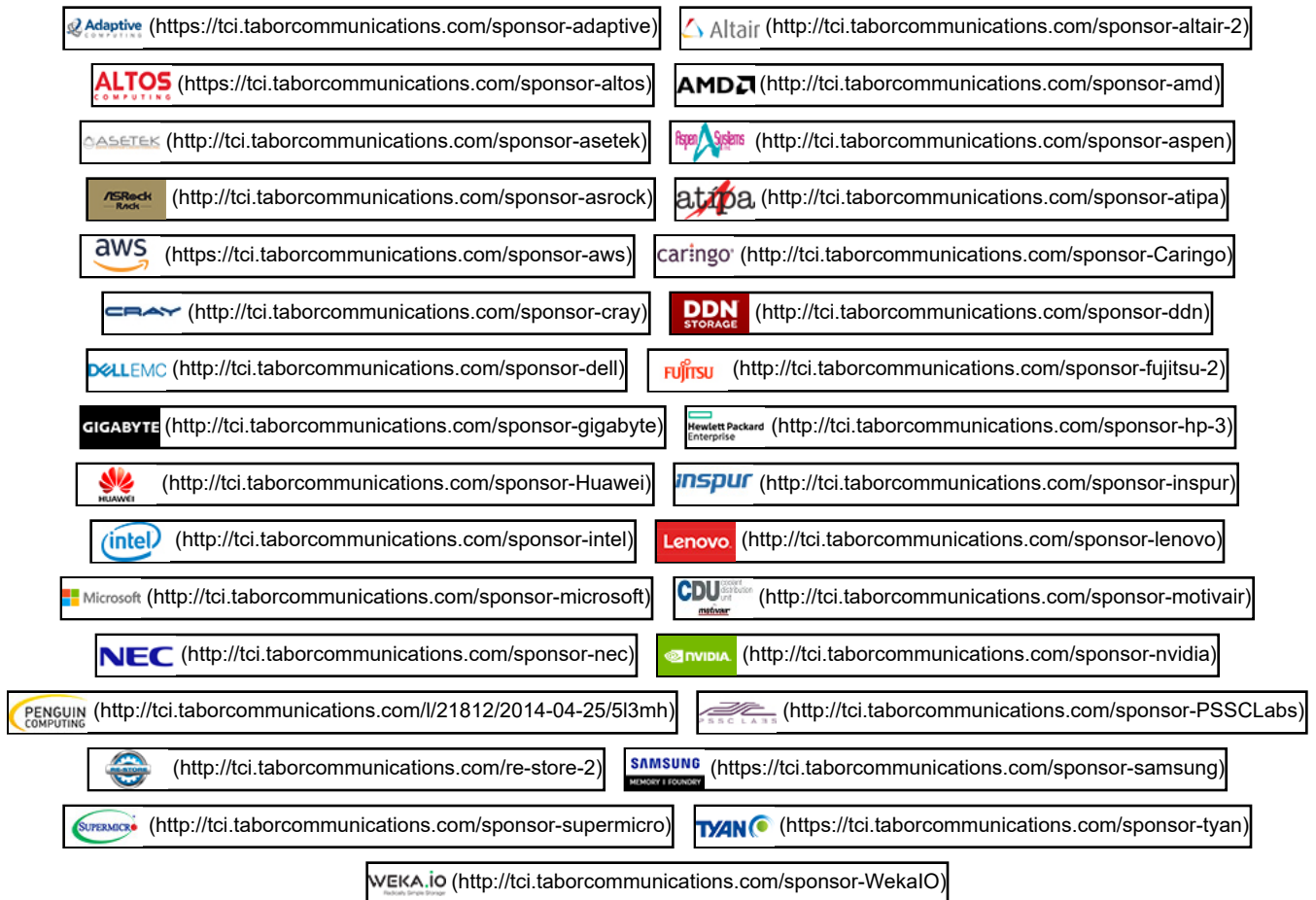
(<https://www.hpcwire.com/2018/09/20/democratization-of-hpc-part1-simulation-sheds-light-on-building-dispute/>)

This is the first of three articles demonstrating the growing acceptance of High Performance Computing especially in new user communities and application areas. Major reason is the [on-building-dispute/](#)

By Wolfgang Gentsch

<http://twitter.com/intent/tweet?status=Democratization%20of%20HPC%20Part%201%3A%20Simulation%20Sheds%20Light%20on%20Building%20Dispute> <http://www.linkedin.com/shareArticle?mini=true&url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F20%2Fdemocratization-of-hpc-part1-simulation-sheds-light-on-building-dispute%2F&title=Democratization%20of%20HPC%20Part%201%3A%20Simulation%20Sheds%20Light%20on%20Building%20Dispute&source=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F20%2Fdemocratization-of-hpc-part1-simulation-sheds-light-on-building-dispute%2F&title=Democratization%20of%20HPC%20Part%201%3A%20Simulation%20Sheds%20Light%20on%20Building%20Dispute> <https://plus.google.com/share?url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F20%2Fdemocratization-of-hpc-part1-simulation-sheds-light-on-building-dispute%2F&title=Democratization%20of%20HPC%20Part%201%3A%20Simulation%20Sheds%20Light%20on%20Building%20Dispute>

Leading Solution Providers



SC17 Booth Video Tours Playlist (<https://www.hpcwire.com/sc17-booth-video-tours/>)



(<https://youtu.be/3nqmmFjEtWk>)

Altair



(<https://youtu.be/yxDsQX26OvA>)

AMD



(<https://youtu.be/X88LSfaem28>)

ASRock Rack



(<https://youtu.be/1ntMQf3k6yE>)

CEJN



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DDN Storage



(<https://youtu.be/loDswtIDIQA>)

Huawei



(<https://youtu.be/OYVcoKHWiTG>)

IBM



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IBM Power Systems



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(<https://youtu.be/ew8GNSxmcDE>)

Lenovo



(<https://youtu.be/sHDnmzBu-2w>)

Mellanox Technologies



(https://youtu.be/01kN4n_VJcM)

Microsoft



(<https://youtu.be/mV1yHMgwn44>)

Penguin Computing



(<https://youtu.be/4C5uBSLDuxc>)

Pure Storage



(<https://youtu.be/xCFnZ9p2SEs>)

Supermicro



(<https://youtu.be/OppODj1rl0c>)

Tyan



(<https://youtu.be/yuTrEkic7kY>)

Univa



Summit Supercomputer is Already Making its Mark on Science

(<https://www.hpcwire.com/2018/09/20/summit-supercomputer-is-already-making-its-mark-on-science/>)

Summit, now the fastest supercomputer in the world, is quickly making its mark in science – five of the six finalists just announced for the prestigious 2018 Gordon Bell Prize use (<https://www.hpcwire.com/2018/09/20/summit-supercomputer-is-already-making-its-mark-on-science/>)

By John Russell

[⦿](http://twitter.com/intent/tweet?status=Summit%20Supercomputer%20is%20Already%20Making%20its%20Mark%20on%20Science+https%3A%2F%2Fwww.hpcwire.com%2F2018%2Fmaking-its-mark-on-science%2F) (<http://twitter.com/intent/tweet?status=Summit%20Supercomputer%20is%20Already%20Making%20its%20Mark%20on%20Science+https%3A%2F%2Fwww.hpcwire.com%2F2018%2Fmaking-its-mark-on-science%2F>) [in](http://www.linkedin.com/shareArticle?mini=true&url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F20-on-science%2F&title=Summit%20Supercomputer%20is%20Already%20Making%20its%20Mark%20on%20Science&source=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F20%2Fsummit-supercomputer-is-already-making-its-mark-on-science%2F) (<http://www.linkedin.com/shareArticle?mini=true&url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F20-on-science%2F&title=Summit%20Supercomputer%20is%20Already%20Making%20its%20Mark%20on%20Science&source=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F20%2Fsummit-supercomputer-is-already-making-its-mark-on-science%2F>) [f](http://www.facebook.com/sharer/sharer.php?u=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F20%2Fsummit-supercomputer-is-already-making-its-mark-on-science%2F&title=Summit%20Supercomputer%20is%20Already%20Making%20its%20Mark%20on%20Science/) (<http://www.facebook.com/sharer/sharer.php?u=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F20%2Fsummit-supercomputer-is-already-making-its-mark-on-science%2F&title=Summit%20Supercomputer%20is%20Already%20Making%20its%20Mark%20on%20Science/>) [G+](https://plus.google.com/share?url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F20%2Fsummit-supercomputer-is-already-making-its-mark-on-science%2F) (<https://plus.google.com/share?url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F20%2Fsummit-supercomputer-is-already-making-its-mark-on-science%2F>)



Migration Tools Needed to Shift ML to Production

(<https://www.hpcwire.com/2018/09/20/migration-tools-needed-to-shift-ml-to-production/>)

The confluence of accelerators like cloud GPUs along with the ability to handle data-rich HPC workloads will help push more machine learning projects into production, concluding migration and accompanying tools. [Read more...](https://www.hpcwire.com/2018/09/20/migration-tools-needed-to-shift-ml-to-production/) (<https://www.hpcwire.com/2018/09/20/migration-tools-needed-to-shift-ml-to-production/>)

By George Leopold

[⦿](http://twitter.com/intent/tweet?status=Migration%20Tools%20Needed%20to%20Shift%20ML%20to%20Production+https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2Fmigration-tools-needed-to-shift-ml-to-production%2F) (<http://twitter.com/intent/tweet?status=Migration%20Tools%20Needed%20to%20Shift%20ML%20to%20Production+https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2Fmigration-tools-needed-to-shift-ml-to-production%2F>) [in](http://www.linkedin.com/shareArticle?mini=true&url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2Fmigration-tools-needed-to-shift-ml-to-production%2F&title=Migration%20Tools%20Needed%20to%20Shift%20ML%20to%20Production&source=https%3A%2F%2Fwww.hpcwire.com/) (<http://www.linkedin.com/shareArticle?mini=true&url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2Fmigration-tools-needed-to-shift-ml-to-production%2F&title=Migration%20Tools%20Needed%20to%20Shift%20ML%20to%20Production&source=https%3A%2F%2Fwww.hpcwire.com/>) [f](http://www.facebook.com/sharer/sharer.php?u=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F20%2Fmigration-tools-needed-to-shift-ml-to-production%2F&title=Migration%20Tools%20Needed%20to%20Shift%20ML%20to%20Production) (<http://www.facebook.com/sharer/sharer.php?u=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F20%2Fmigration-tools-needed-to-shift-ml-to-production%2F&title=Migration%20Tools%20Needed%20to%20Shift%20ML%20to%20Production>) [G+](https://plus.google.com/share?url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F20%2Fmigration-tools-needed-to-shift-ml-to-production%2F) (<https://plus.google.com/share?url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F20%2Fmigration-tools-needed-to-shift-ml-to-production%2F>)



Kyoto University ACCMS Implements Fine-grained Power Management

(<https://www.hpcwire.com/2018/09/19/kyoto-university-accms-implements-fine-grained-power-management/>)

Data center power management is a ubiquitous challenge and in few places is it more so than at Kyoto University Academic Center for Computing and Media Studies (ACCMS) devastating 2011 Tohoku earthquake and tsunami. [Read more...](https://www.hpcwire.com/2018/09/19/kyoto-university-accms-implements-fine-grained-power-management/)

By Staff

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[!\[\]\(4d1d3f2547aeece54bb6babd23f4121b_img.jpg\) \(http://www.linkedin.com/shareArticle?mini=true&url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F19%2Fkyoto-university-accms-implements-fine-grained-power-management%2F&title=Kyoto%20University%20ACCMS%20Implements%20Fine-grained%20Power%20Management&source=https%3A%2F%2Fwww](http://www.linkedin.com/shareArticle?mini=true&url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F19%2Fkyoto-university-accms-implements-fine-grained-power-management%2F&title=Kyoto%20University%20ACCMS%20Implements%20Fine-grained%20Power%20Management&source=https%3A%2F%2Fwww)

[!\[\]\(ec45aa71601db5755c5e2662ad427708_img.jpg\) \(http://www.facebook.com/sharer/sharer.php?u=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F19%2Fkyoto-university-accms-implements-fine-grained-power-management%2F&title=Kyoto%20University%20ACCMS%20Implements%20Fine-grained%20Power%20Management/\)](http://www.facebook.com/sharer/sharer.php?u=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F19%2Fkyoto-university-accms-implements-fine-grained-power-management%2F&title=Kyoto%20University%20ACCMS%20Implements%20Fine-grained%20Power%20Management/)

[!\[\]\(8f6ad92394b094baf6a51f98af6c5abc_img.jpg\) \(https://plus.google.com/st/url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F19%2Fkyoto-university-accms-implements-fine-grained-power-management%2F\)](https://plus.google.com/st/url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F19%2Fkyoto-university-accms-implements-fine-grained-power-management%2F)



What's New in HPC Research: September (Part 1)

(<https://www.hpcwire.com/2018/09/18/whats-new-in-hpc-research-september-part-1/>)

In this new bimonthly feature, *HPCwire* will highlight newly published research in the high-performance computing community and related domains. [Read more...](https://www.september-part-1/) (<https://www.september-part-1/>)

By Oliver Peckham

[ⓧ](http://twitter.com/intent/tweet?status=What%26%238217%3Bs%20New%20in%20HPC%20Research%3A%20September%20%28Part%201%29+https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F18%2Fwhats-new-in-hpc-research-september-part-1%2F) (<http://twitter.com/intent/tweet?status=What%26%238217%3Bs%20New%20in%20HPC%20Research%3A%20September%20%28Part%201%29+https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F18%2Fwhats-new-in-hpc-research-september-part-1%2F>)

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[f](http://www.facebook.com/sharer/sharer.php?u=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F18%2Fwhats-new-in-hpc-research-september-part-1%2F&title=What%26%238217%3Bs%20New%20in%20HPC%20Research%3A%20September%20%28Part%201%29/) (<http://www.facebook.com/sharer/sharer.php?u=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F18%2Fwhats-new-in-hpc-research-september-part-1%2F&title=What%26%238217%3Bs%20New%20in%20HPC%20Research%3A%20September%20%28Part%201%29/>)

[G+](https://plus.google.com/s?url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F18%2Fwhats-new-in-hpc-research-september-part-1%2F) (<https://plus.google.com/s?url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F18%2Fwhats-new-in-hpc-research-september-part-1%2F>)



House Passes \$1.275B National Quantum Initiative

(<https://www.hpcwire.com/2018/09/17/house-passes-1-275b-national-quantum-initiative/>)

Last Thursday the U.S. House of Representatives passed the National Quantum Initiative Act (NQIA (<https://www.congress.gov/bill/115th-congress/house-bill/6227/text>)) intended for quantum computing development. [Read more...](https://www.hpcwire.com/2018/09/17/house-passes-1-275b-national-quantum-initiative/) (<https://www.hpcwire.com/2018/09/17/house-passes-1-275b-national-quantum-initiative/>)

By John Russell

[ⓧ](http://twitter.com/intent/tweet?status=House%20Passes%20%241.275B%20National%20Quantum%20Initiative+https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F17%2Fhouse-passes-1-275b-national-quantum-initiative%2F) (<http://twitter.com/intent/tweet?status=House%20Passes%20%241.275B%20National%20Quantum%20Initiative+https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F17%2Fhouse-passes-1-275b-national-quantum-initiative%2F>)

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[f](http://www.facebook.com/sharer/sharer.php?u=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F17%2Fhouse-passes-1-275b-national-quantum-initiative%2F&title=House%20Passes%20%241.275B%20National%20Quantum%20Initiative) (<http://www.facebook.com/sharer/sharer.php?u=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F17%2Fhouse-passes-1-275b-national-quantum-initiative%2F&title=House%20Passes%20%241.275B%20National%20Quantum%20Initiative>)

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Nvidia Accelerates AI Inference in the Datacenter with T4 GPU

(<https://www.hpcwire.com/2018/09/14/nvidia-accelerates-ai-inference-in-the-datacenter-with-t4-gpu/>)

Nvidia is upping its game for AI inference in the datacenter with a new platform consisting of an inference accelerator chip—the new Turing-based Tesla T4 GPU—and a refresh microservice. [Read more...](https://www.hpcwire.com/2018/09/14/nvidia-accelerates-ai-inference-in-the-datacenter-with-t4-gpu/) (<https://www.hpcwire.com/2018/09/14/nvidia-accelerates-ai-inference-in-the-datacenter-with-t4-gpu/>).

By George Leopold

[⦿](http://twitter.com/intent/tweet?status=Nvidia%20Accelerates%20AI%20Inference%20in%20the%20Datacenter%20with%20T4%20GPU+https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F14%2Fnvidia-accelerates-ai-inference-in-the-datacenter-with-t4-gpu%2F) ([http://twitter.com/intent/tweet?](http://twitter.com/intent/tweet?status=Nvidia%20Accelerates%20AI%20Inference%20in%20the%20Datacenter%20with%20T4%20GPU+https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F14%2Fnvidia-accelerates-ai-inference-in-the-datacenter-with-t4-gpu%2F)

[in](http://www.facebook.com/sharer/sharer.php?u=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F14%2Fnvidia-accelerates-ai-inference-in-the-datacenter-with-t4-gpu%2F&title=Nvidia%20Accelerates%20AI%20Inference%20in%20the%20Datacenter%20with%20T4%20GPU&source=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F14%2Fnvidia-accelerates-ai-inference-in-the-datacenter-with-t4-gpu%2F) (<http://www.linkedin.com/shareArticle?mini=true&url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F14%2Fnvidia-accelerates-ai-inference-in-the-datacenter-with-t4-gpu%2F&title=Nvidia%20Accelerates%20AI%20Inference%20in%20the%20Datacenter%20with%20T4%20GPU&source=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F14%2Fnvidia-accelerates-ai-inference-in-the-datacenter-with-t4-gpu%2F>

[\(http://www.facebook.com/sharer/sharer.php?u=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F14%2Fnvidia-accelerates-ai-inference-in-the-datacenter-with-t4-gpu%2F&title=Nvidia%20Accelerates%20AI%20Inference%20in%20the%20Datacenter%20with%20T4%20GPU/\)](http://www.facebook.com/sharer/sharer.php?u=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F14%2Fnvidia-accelerates-ai-inference-in-the-datacenter-with-t4-gpu%2F&title=Nvidia%20Accelerates%20AI%20Inference%20in%20the%20Datacenter%20with%20T4%20GPU/) [G+](https://plus.google.com/share?url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F14%2Fnvidia-accelerates-ai-inference-in-the-datacenter-with-t4-gpu%2F) ([https://plus.google.com/share?](https://plus.google.com/share?url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F14%2Fnvidia-accelerates-ai-inference-in-the-datacenter-with-t4-gpu%2F)

[url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F14%2Fnvidia-accelerates-ai-inference-in-the-datacenter-with-t4-gpu%2F](https://plus.google.com/share?url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F14%2Fnvidia-accelerates-ai-inference-in-the-datacenter-with-t4-gpu%2F))



NSF Highlights Expanded Efforts for Broadening Participation in Computing

(<https://www.hpcwire.com/2018/09/13/nsf-highlights-expanded-efforts-for-broadening-participation-in-computing/>)

Today, the Directorate of Computer and Information Science and Engineering (CISE) of the NSF released a letter highlighting the expansion of its broadening participation in computing. (<https://www.hpcwire.com/2018/09/13/nsf-highlights-expanded-efforts-for-broadening-participation-in-computing/>)

By Staff

[⦿](http://twitter.com/intent/tweet?status=NSF%20Highlights%20Expanded%20Efforts%20for%20Broadening%20Participation%20in%20Computing+https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F13%2Fnsf-highlights-expanded-efforts-for-broadening-participation-in-computing%2F) ([http://twitter.com/intent/tweet?](http://twitter.com/intent/tweet?status=NSF%20Highlights%20Expanded%20Efforts%20for%20Broadening%20Participation%20in%20Computing+https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F13%2Fnsf-highlights-expanded-efforts-for-broadening-participation-in-computing%2F)

[status=NSF%20Highlights%20Expanded%20Efforts%20for%20Broadening%20Participation%20in%20Computing+https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F13%2Fnsf-highlights-expanded-efforts-for-broadening-participation-in-computing%2F](http://www.facebook.com/sharer/sharer.php?u=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F13%2Fnsf-highlights-expanded-efforts-for-broadening-participation-in-computing%2F&title=NSF%20Highlights%20Expanded%20Efforts%20for%20Broadening%20Participation%20in%20Computing)) [in](http://www.linkedin.com/shareArticle?mini=true&url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F13%2Fnsf-highlights-expanded-efforts-for-broadening-participation-in-computing%2F&title=NSF%20Highlights%20Expanded%20Efforts%20for%20Broadening%20Participation%20in%20Computing&source=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F13%2Fnsf-highlights-expanded-efforts-for-broadening-participation-in-computing%2F) (<http://www.linkedin.com/shareArticle?mini=true&url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F13%2Fnsf-highlights-expanded-efforts-for-broadening-participation-in-computing%2F&title=NSF%20Highlights%20Expanded%20Efforts%20for%20Broadening%20Participation%20in%20Computing&source=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F13%2Fnsf-highlights-expanded-efforts-for-broadening-participation-in-computing%2F>) [\(http://www.facebook.com/sharer/sharer.php?u=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F13%2Fnsf-highlights-expanded-efforts-for-broadening-participation-in-computing%2F&title=NSF%20Highlights%20Expanded%20Efforts%20for%20Broadening%20Participation%20in%20Computing/\)](http://www.facebook.com/sharer/sharer.php?u=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F13%2Fnsf-highlights-expanded-efforts-for-broadening-participation-in-computing%2F&title=NSF%20Highlights%20Expanded%20Efforts%20for%20Broadening%20Participation%20in%20Computing/) [G+](https://plus.google.com/u/0/share?url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F13%2Fnsf-highlights-expanded-efforts-for-broadening-participation-in-computing%2F) (<https://plus.google.com/u/0/share?url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F13%2Fnsf-highlights-expanded-efforts-for-broadening-participation-in-computing%2F>)



DeepSense Combines HPC and AI to Bolster Canada's Ocean Economy

(<https://www.hpcwire.com/2018/09/13/deepsense-combines-hpc-and-ai-to-bolster-canadas-emerging-ocean-economy/>)

We often hear scientists say that we know less than 10 percent of the life of the oceans. [Read more...](https://www.hpcwire.com/2018/09/13/deepsense-combines-hpc-and-ai-) (<https://www.hpcwire.com/2018/09/13/deepsense-combines-hpc-and-ai->

By Tiffany Trader

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TACC Podcast Discusses Frontera Supercomputer with NSF's Manish Parashar

(<https://www.hpcwire.com/2018/09/12/tacc-podcast-discusses-frontera-supercomputer-with-nsfs-manish-parashar/>)

On August 29, the NSF and TACC announced that Stampede 2 would be augmented with a new system called Frontera, which will become the new fastest university supercomputer for acquisition and deployment of Frontera, which has an anticipated peak performance between 35-40 petaflops and is expected to begin operations in 2019. [Read more \(discusses-frontera-supercomputer-with-nsfs-manish-parashar/\)](#)

By Staff

<http://twitter.com/intent/tweet?status=TACC%20Podcast%20Discusses%20Frontera%20Supercomputer%20with%20NSF%26%238217%3Bs%20Manish%20Parashar+https%3A%2F%2Fpodcast-discusses-frontera-supercomputer-with-nsfs-manish-parashar%2F> <http://www.linkedin.com/shareArticle?mini=true&url=https%3A%2F%2Fpodcast-discusses-frontera-supercomputer-with-nsfs-manish-parashar%2F&title=TACC%20Podcast%20Discusses%20Frontera%20Supercomputer%20with%20NSF%26%238217%3Bs%20Manish%20Parashar&source=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F12%2Ftacc-podcast-discusses-frontera-super-parashar%2F&title=TACC%20Podcast%20Discusses%20Frontera%20Supercomputer%20with%20NSF%26%238217%3Bs%20Manish%20Parashar/> <https://plus.url=https%3A%2F%2Fwww.hpcwire.com%2F2018%2F09%2F12%2Ftacc-podcast-discusses-frontera-supercomputer-with-nsfs-manish-parashar%2F>



Google Releases Machine Learning “What-If” Analysis Tool

<https://www.hpcwire.com/2018/09/12/google-releases-machine-learning-what-if-analysis-tool/>

Training machine learning models has long been time-consuming process. Yesterday, Google released a “What-If Tool” for probing how data point changes affect a model’s pre open source TensorBoard web application... [Read more...](https://www.hpcwire.com/2018/09/12/google-releases-machine-learning-what-if-analysis-tool/) (<https://www.hpcwire.com/2018/09/12/google-releases-machine-learning-what-if-analysis-tool/>)

By John Russell

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